



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Lawrence S. Uzelac, et al. Art Unit: 2116

Serial No.: 10/625,584

Examiner: James K. Trujillo

Assignee

: Intel Corporation

Filed

: July 22, 2003

Title

: METHOD AND APPARATUS FOR REDUCING SUB-THRESHOLD OFF

CURRENT FOR A REAL TIME CLOCK CIRCUIT DURING BATTERY

**OPERATION** 

Mail Stop Amendment Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

## **AMENDMENT**

In response to the Office action mailed January 6, 2005, please amend the application as follows: Amendments to the Claims begin on page 2 of this paper. Remarks/Arguments begin on page 9 of this paper.

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## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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